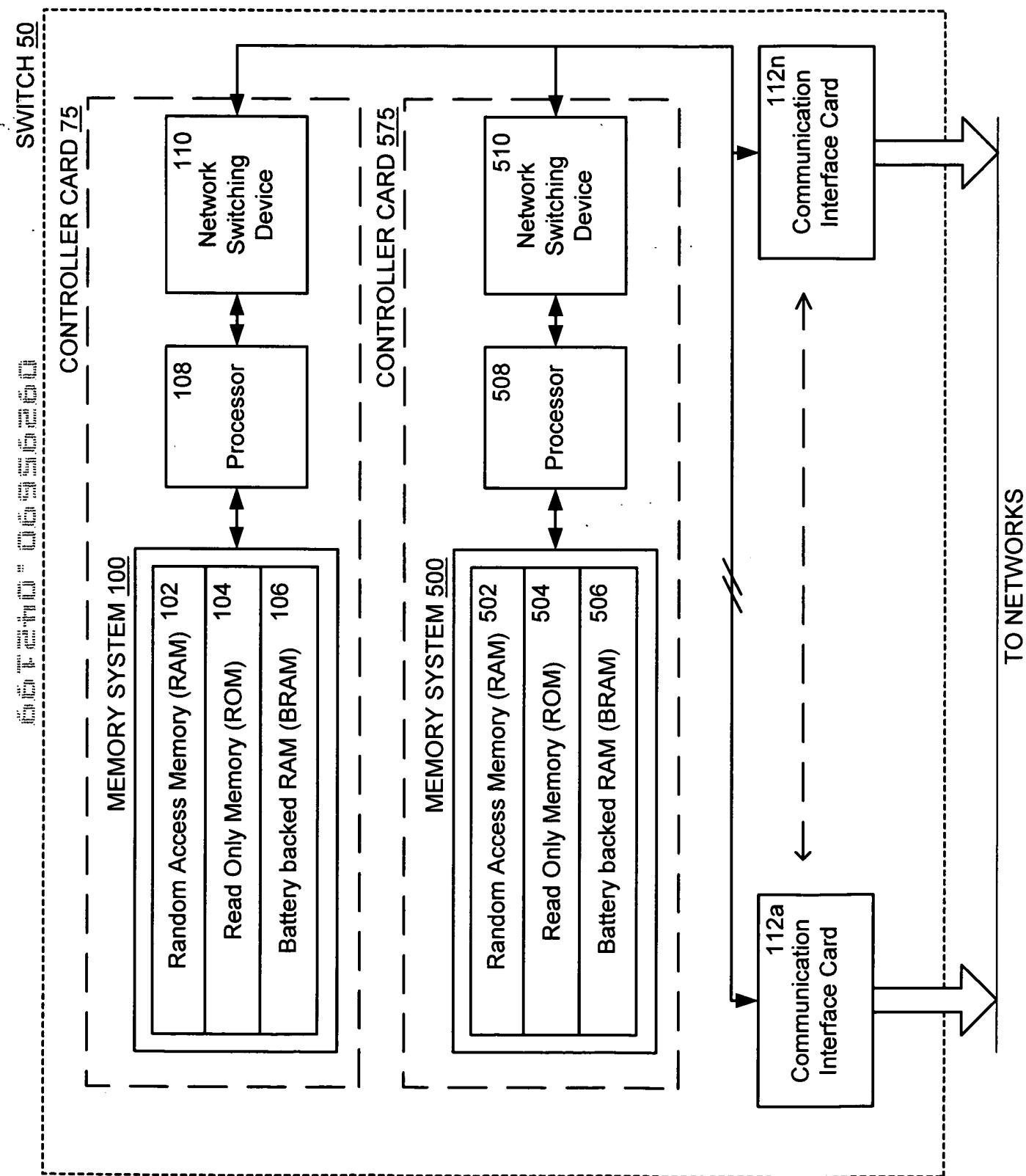
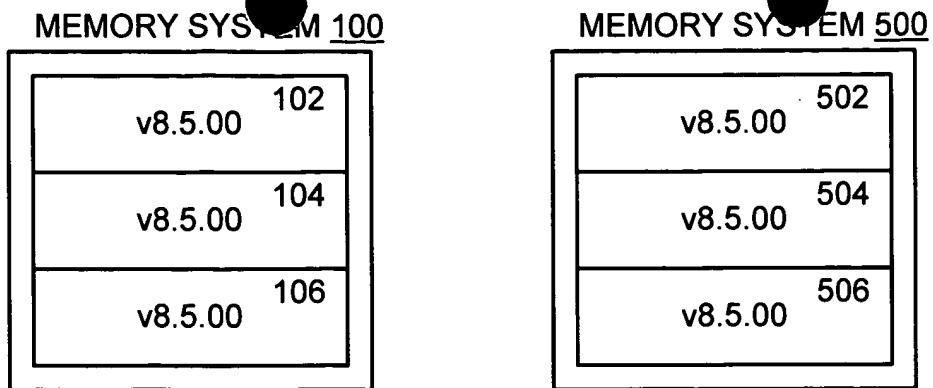
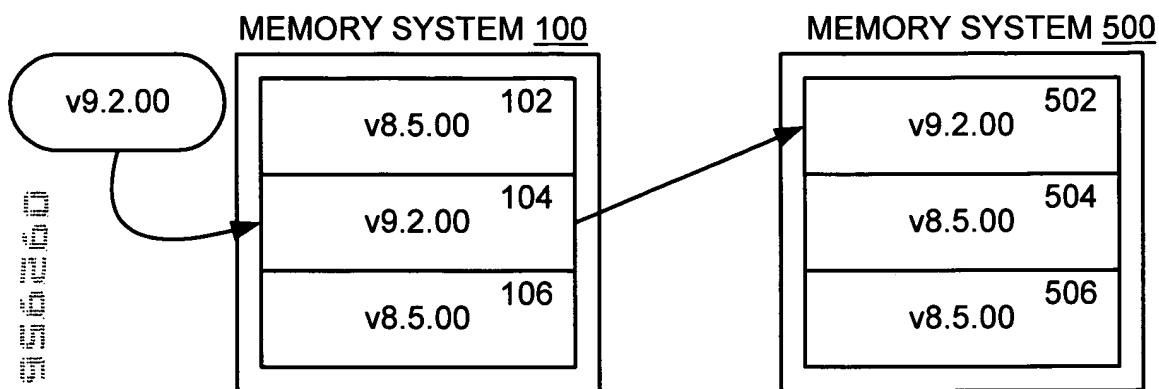


Fig. 1

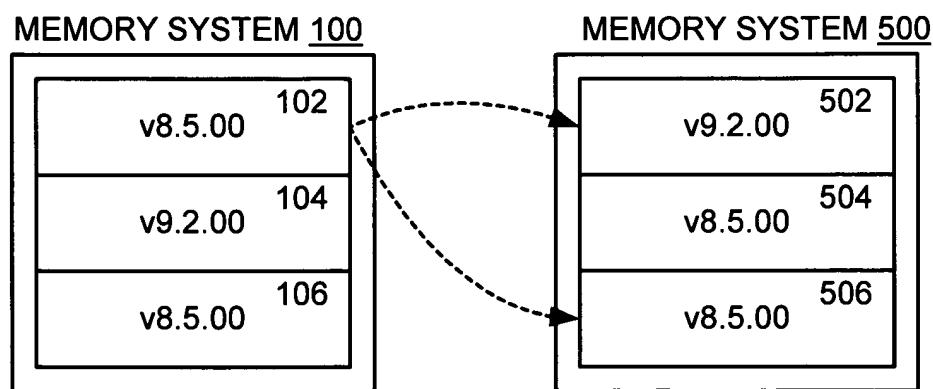




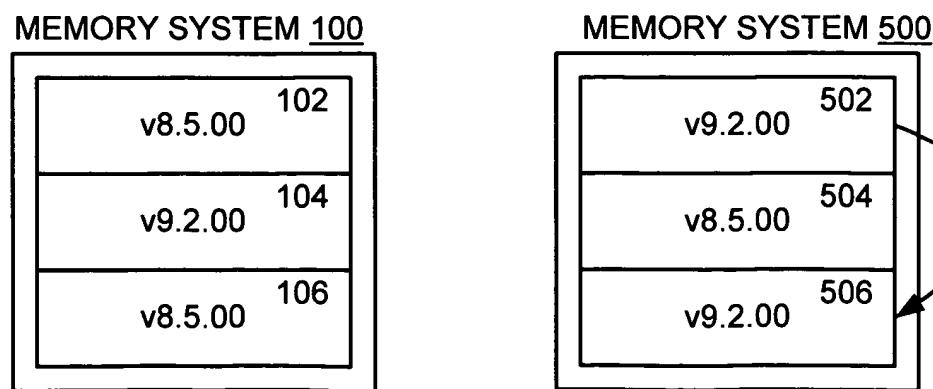
**Fig. 2a**



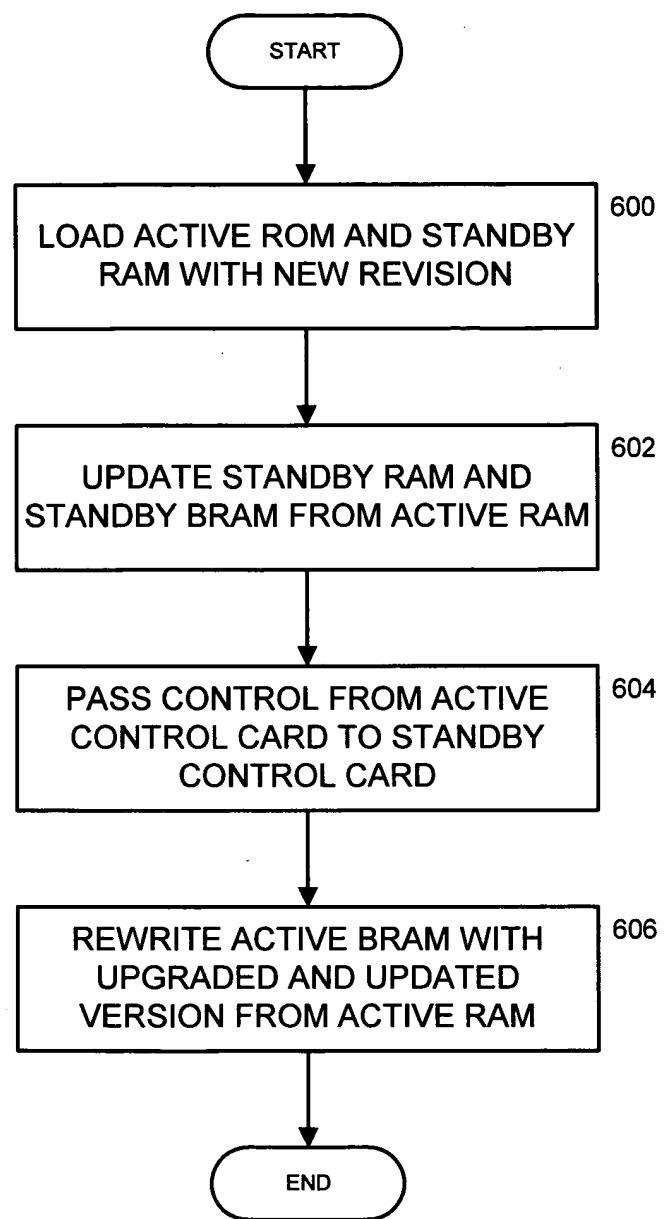
**Fig. 2b**



**Fig. 2c**



**Fig. 2d**



**Fig. 3**

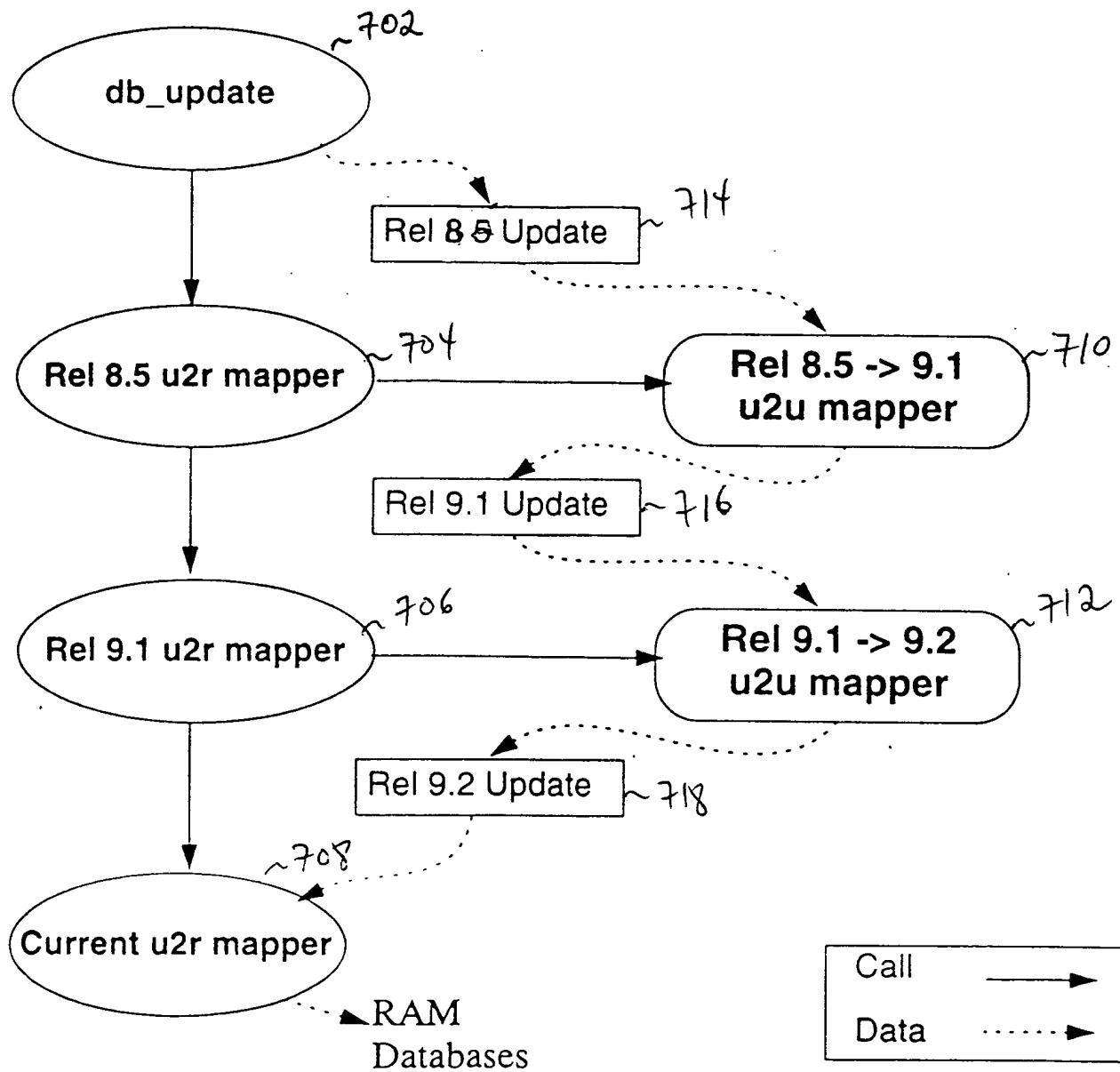
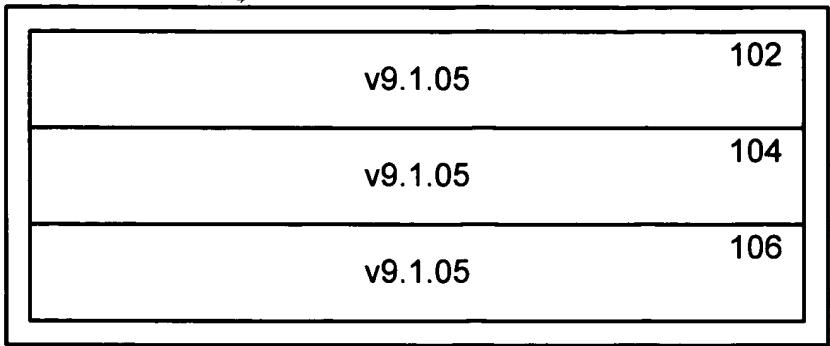


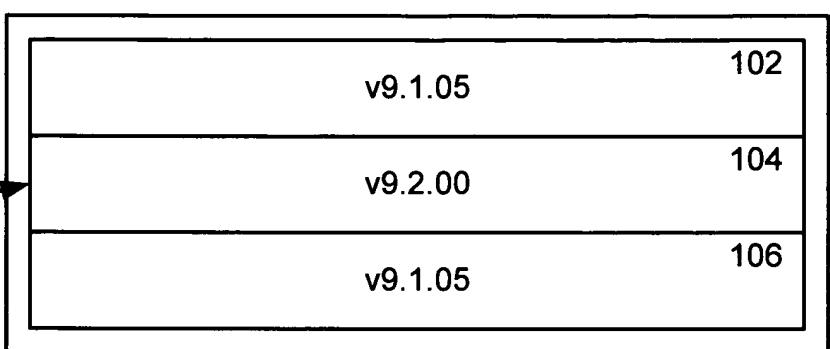
FIG. 4

MEMORY SYSTEM 100



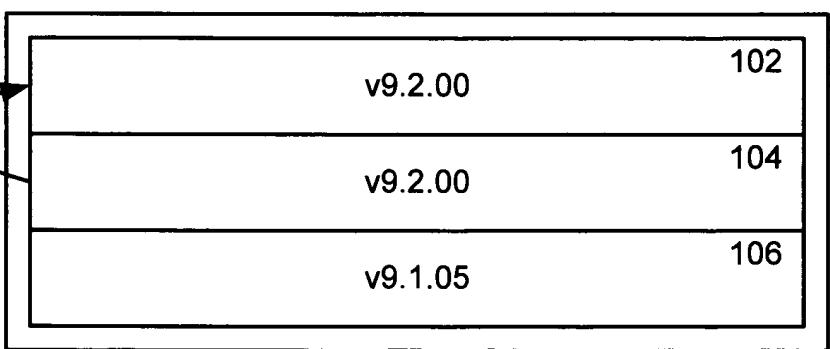
**Fig. 5a**

MEMORY SYSTEM 100



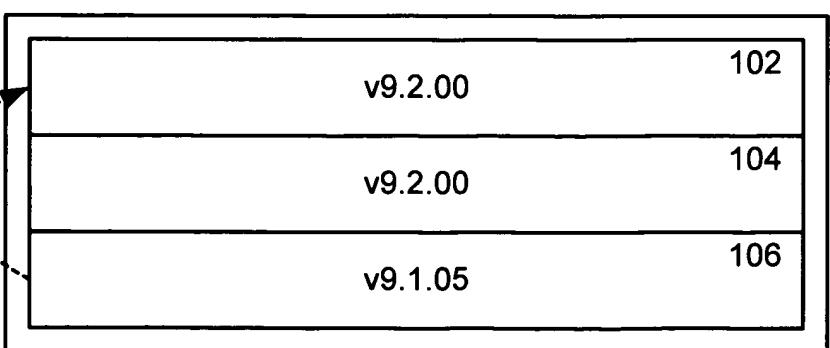
**Fig. 5b**

MEMORY SYSTEM 100



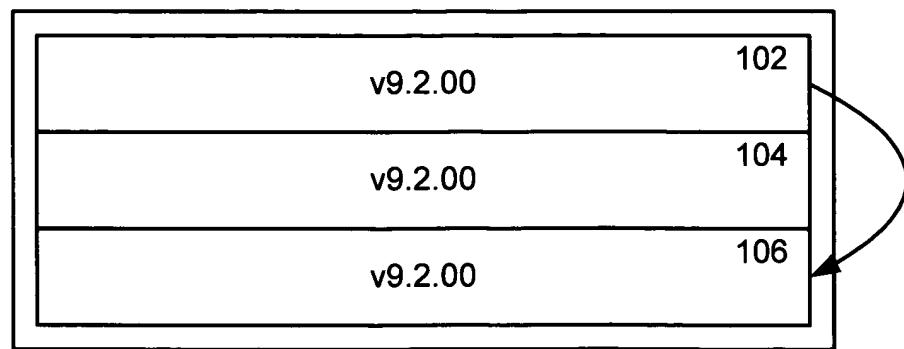
**Fig. 5c**

MEMORY SYSTEM 100

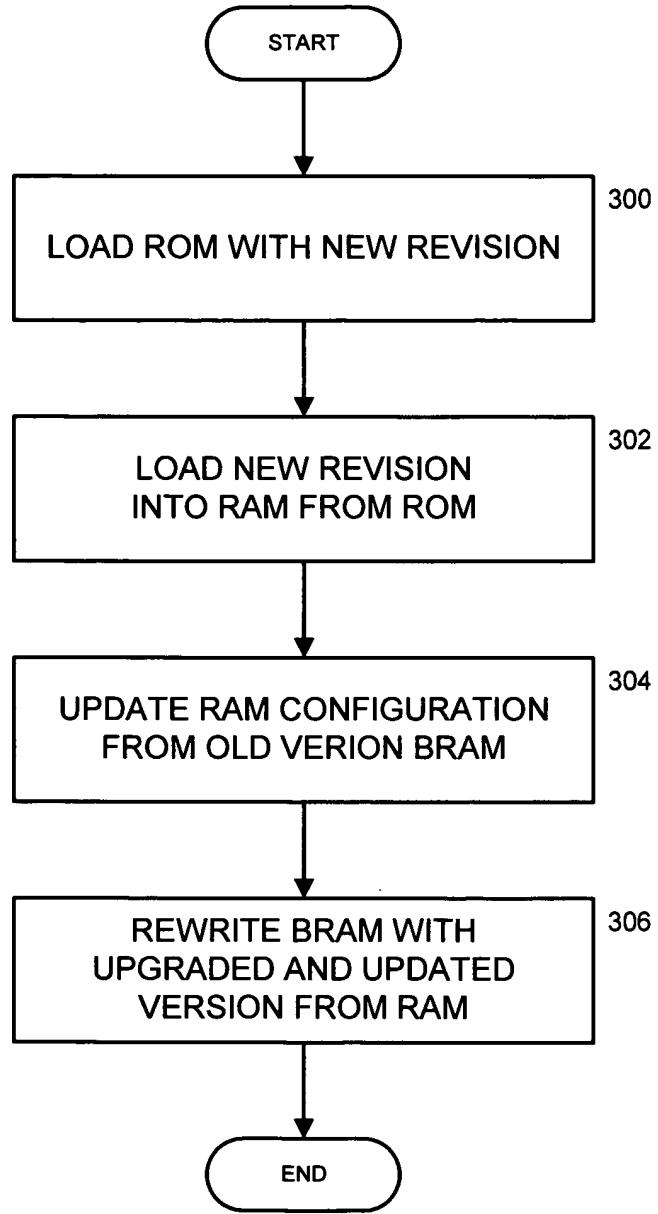


**Fig. 5d**

**MEMORY SYSTEM 100**



**Fig. 5e**



**Fig. 6**

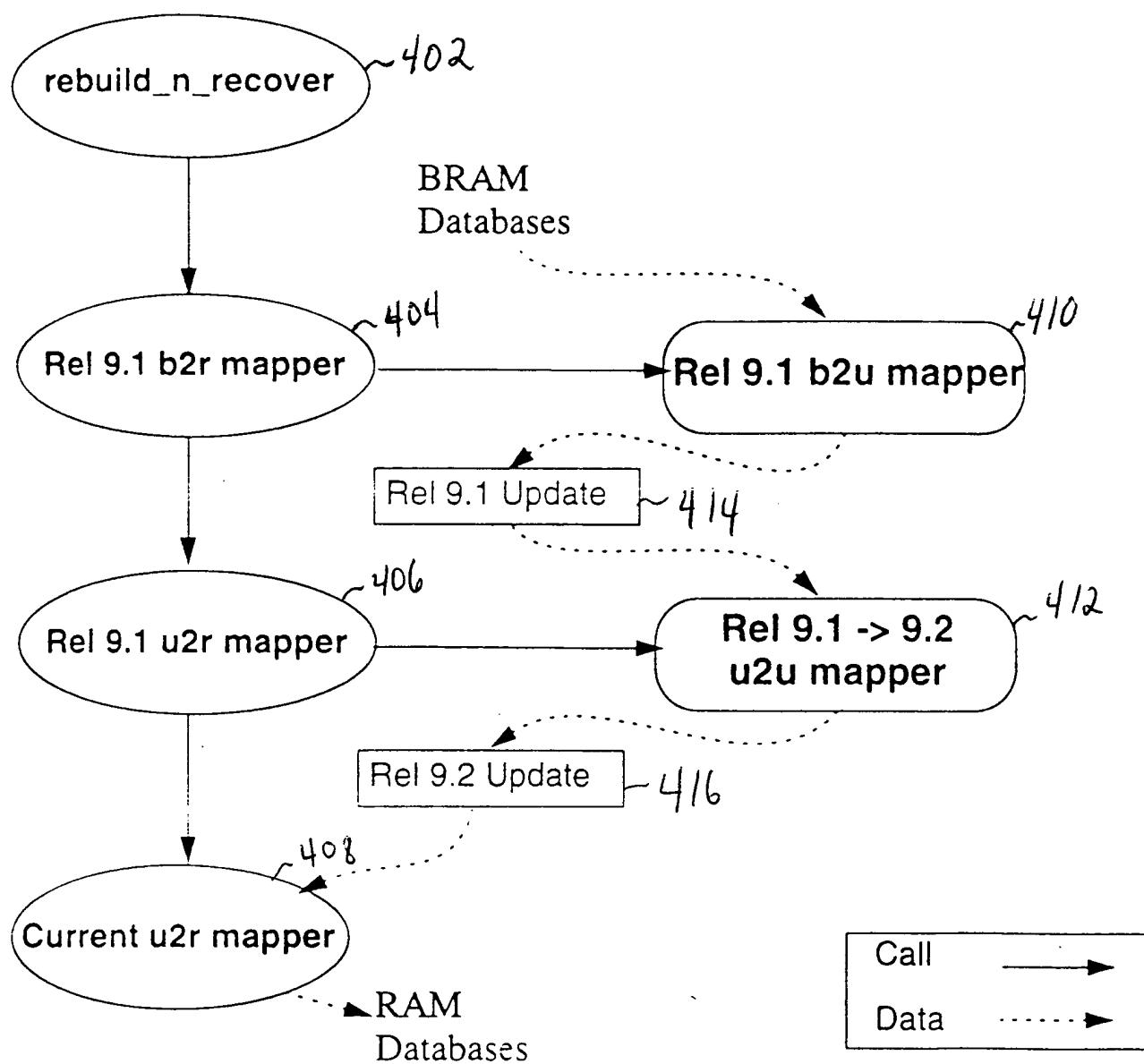


FIG. 7